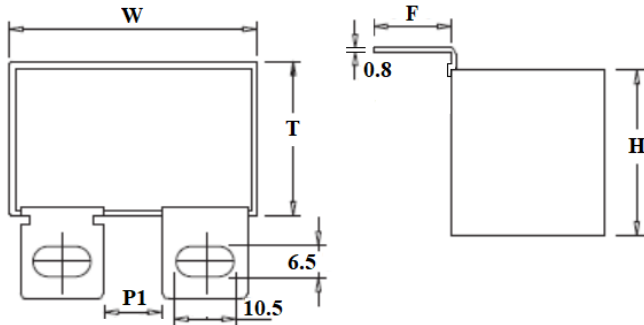


SMIT series

■ Outline Drawing



■ Typical Applications

These capacitors are used in high voltage, high current

And high pulse applications such as :

IGBT protection circuits

Snubber networks

Energy conversion and control in power electronics

Protection circuits in SMPS

■ Features

Double sided metalized polypropylene film

Small structure loss

Small internal temperature rise and

excellent Flame retardancy

Flame retardant UL94 - V0, RoHS compliant

■ Specifications

■ Construction



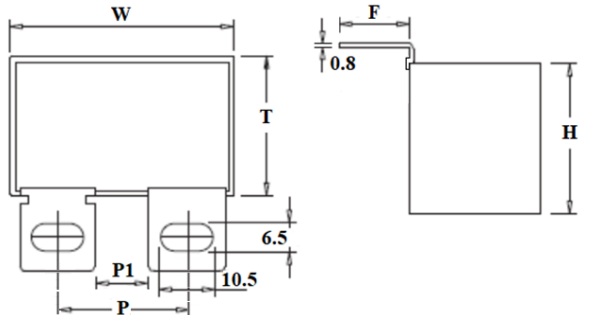
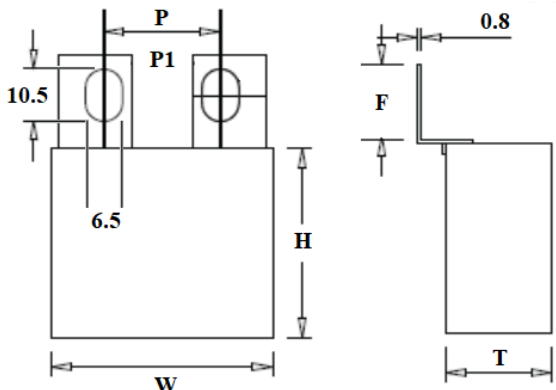
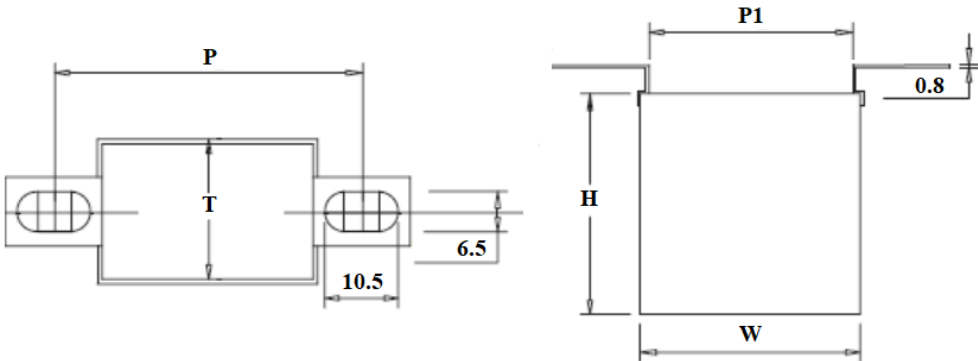
Reference Standard	IEC 61071				
Climatic Category	40/85/56/B				
Operating Temperature Range	85°C for U_R (dc) ; 75°C for U_R (ac)				
Capacitance Range	0.047 μ F ~ 9.0 μ F				
Rated (DC) Voltage	630Vdc	700Vdc	800Vdc	1000Vdc	1200Vdc
	1600Vdc	2000Vdc	2500Vdc	3000Vdc	--
Capacitance Tolerance	$\pm 5\%$ (J) 、 $\pm 10\%$ (K)				
Voltage Proof	1.6 U_R (10s)				
Dissipation Factor	$\leq 0.2\%$ (25°C, 1kHz)				
Insulation Resistance	IR $\geq 15,000M\Omega$ (20°C, 100V, 1min)				

SMIT series

■ Product code system

SMIT	F	205	K	0700	D	1	26	DP
Type	Internal use	Capacitance	Tolerance	Rated Voltage	Voltage	Lead forming	Lead Pitch	Lead Length
SMIT= IGBT Snubber Capacitors (Boxed)	--	473 =47000pF =47nF =0.047μF	J=±5% K=±10% M=±20%	0630=630Vdc 0700=700Vdc 1000=1000Vdc 1600=1600Vdc 2000=2000Vdc	D=DC A=AC	Shown as Table I	26=26mm 35=35mm 40=40mm	DP= Flat quick-connectors

■ Table I

Style	1	2
Lead Forming	 <p>Diagram showing lead forming for style 1. Dimensions include W, T, H, F, 0.8, 6.5, P1, and P.</p>	 <p>Diagram showing lead forming for style 2. Dimensions include P, P1, 10.5, 6.5, H, W, T, F, and 0.8.</p>
Style	3	
Lead Forming	 <p>Diagram showing lead forming for style 3. Dimensions include P, T, H, 10.5, 6.5, P1, W, and 0.8.</p>	

SMIT series

■ Specifications

Test items	Performance	Test Method
Withstand voltage (Between Terminals)	Shall be no abnormality	160% of rated voltage, 10sec
Between terminal and Enclosure	Shall be no abnormality	$U_R \times 200\% + 1000V_{ac}$, 60sec.
Insulation resistance (Between Terminals)	$C_R \leq 0.33\mu F$, $IR \geq 15000M\Omega$ $C_R > 0.33\mu F$, $IR \geq 5000s$	100Vdc, 60sec / 20°C
Capacitance	Within the tolerance specified	1KHz, 1Vrms Max. at 25°C
Dissipation Factor	0.002 (0.2%) Max.	1Vrms Max. at 25°C

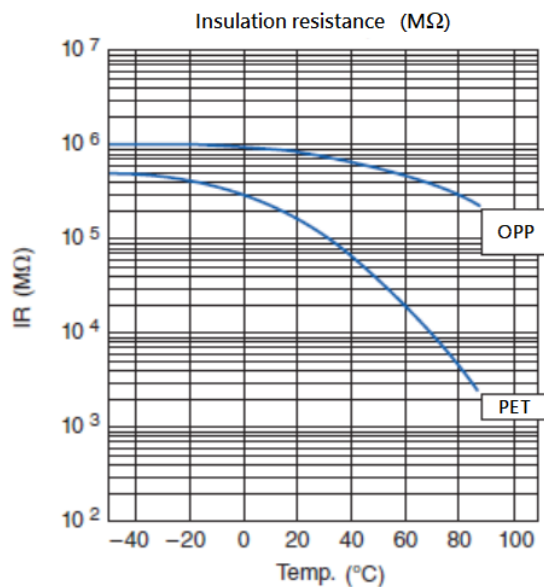
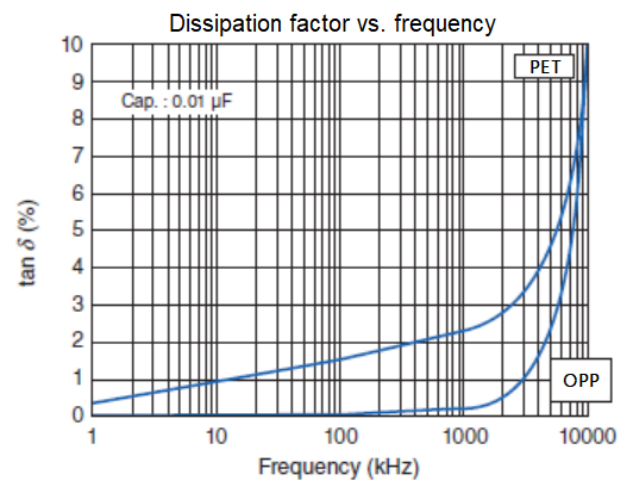
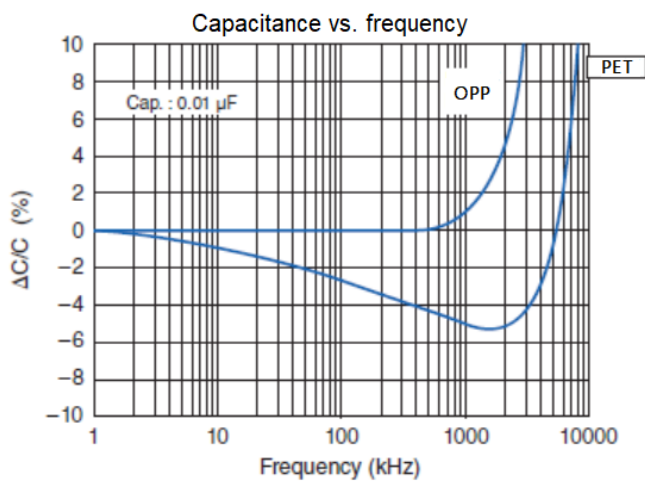
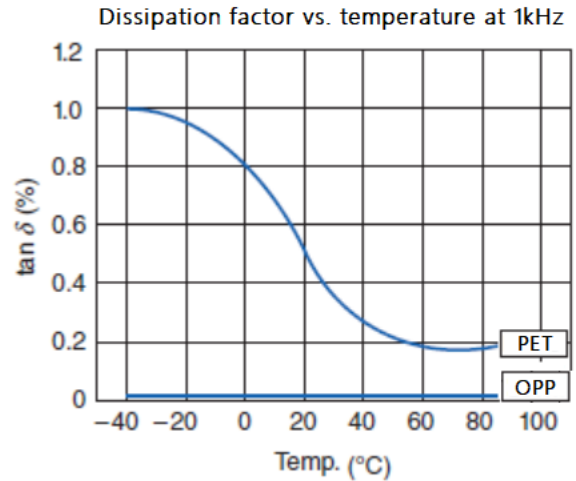
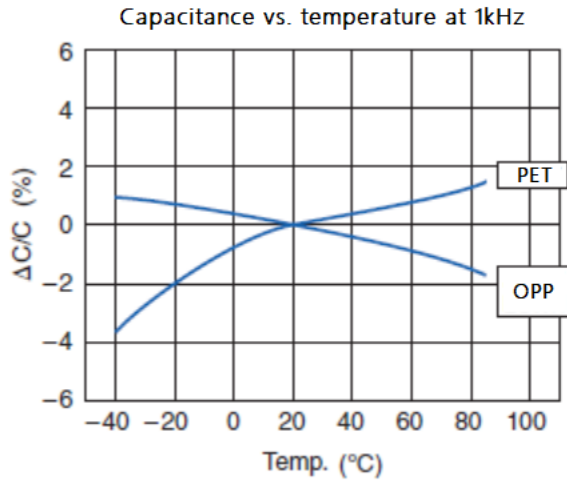
■ Mark



1. WDC is a registered trademark of WINDAY	2. Capacitance: 205 indicates 2.0 μF
3. Capacitors Tolerance: $K = \pm 10\%$	4. Rated Voltage: 700Vdc, Indicates 700
5. MC for Double sided metallized polypropylene film	

SMIT series

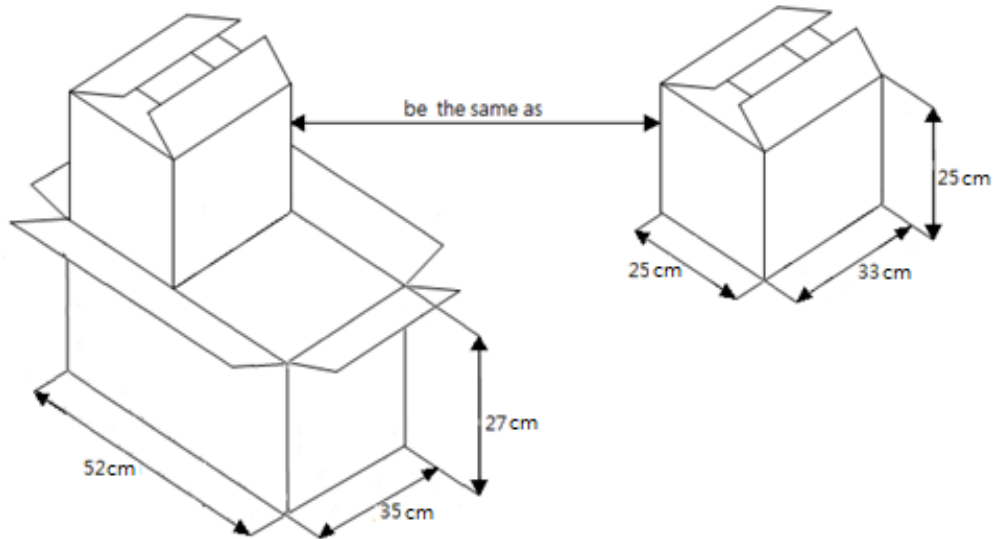
■ Typical graphs



PET :	Metallized Polyester film capacitor
OPP :	Metallized Polypropylene film capacitor

SMIT series

■ Packaging



Size (mm)	Pcs / Bag	Pcs / Inner carton (L33:cm XH:25cm X T:25cm)	Pcs / Out box (L52:cm XH:27cm X T:35cm)
18 * 12 * 6	500	5000	10000

■ Storage conditions and duration

Packaged capacitors should be kept in clean, ventilated, dry coffers, not near the heat source, not subject to direct sunlight, is strictly prohibited and chemical reagents, acid and harmful gas storage together.

Capacitor at a temperature within the range 20 ~ 25 °C, humidity less than 50% of the state of storage for one year.